

U.S. UTILITY Patent Application

PATENT NUMBER	ISSUE DATE
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APPL NUM 10040122	FILING DATE 01/02/2002	CLASS 34516	SUBCLASS 4	GAU 2104	EXAMINER GAYBOWSKI
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2153
2825

**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's Initials		ATTORNEY DOCKET NO FIS9-2001-0396US1
TITLE : Method for identifying test points to optimize the testing of integrated circuits using a genetic algorithm		

U.S. DEPT. OF COMMERCE PAT & TM-PTO-435 (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
		Sheets Drawg.	Figs. Drawg.
Amount Due		Print Fig.	
Date Paid			
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	
PREPARED FOR ISSUE			
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